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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | Document reference | | | | | | | |
|  | MC8704 PTS v5.1 (table 4-1) | | MC7354 PTS v2 (table 3-1) | | MC Hardware integration guide v5 (table 4-1) | | Migration Guide from AirPrime MC879x to AirPrime MC8704/MC8705 v2 (tables 6 & 11) | |
| Pin Number | Label | Direction indicated | Label | Direction indicated | Label | Direction indicated | Label | Direction |
| 45 | CLK | Input | CLK | Output | CLK | Input | ClK | Output |
| 47 | DOUT | Output | DOUT | Output | DIN | Input | DOUT | Output |
| 49 | DIN | Input | DIN | Input | DOUT | Output | DIN | Input |
| 51 | WS | Input | WS | Input\* | WS | Input | WS | Output |

Notes

* MC8704 is I2S slave, MC73xx is I2S master hence clock direction is different.
* Labelling between 47 and 49 has changed because of the master/slave change but so has the direction.
* \*Should be named Input/output depending on mode. For I2S it will be output, for PCM which can be either master or slave it will depend on the mode.
* Text in red is incorrect and needs to be modified.